## DECLARATION FOR PATENT APPLICATION (WITH POWER OF ATTORNEY)

	As an inventor named below or on any attack My residence, post office address and citizer I believe that I am the original, first and sole below) of the subject matter which is claimed an STRUCTURES FOR SEMICONDUCTOR DEV	iship are as stated next to my name.  inventor (if only one name is listed below) or  door which a patent is sought on the invention	a entitik	ed Keinforced, self-align	ATTAC COMPO	CIIVE		
	Ø is attached hereto.  □ was filed on as United States application serial no and was amended on							
	was filed on as PCT international application no and was amended under PCT Article 19 on							
	I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.							
	I acknowledge the duty to disclose to the U.S. Patent and Trademark Office all information known to me to be material to the patentability of the subject matter claimed in this application, as "materiality" is defined in Title 37, Code of Federal Regulations § 1.56.							
	I hereby claim foreign priority benefits under Title 35, United States Code, § 119(a)-(d) or § 365(b) of any foreign application(s) for patent or inventor's certificate or § 365(a) of any PCT international application(s) designating at least one country other than the United States of America listed below and on any attached continuation page and have also identified below and on any attached continuation page any foreign application for patent or inventor's certificate or any PCT international application(s) designating at least one country other than the United States of America having a filing date before that of the application(s) on which priority is claimed.							
	Prior foreign/PCT application(a):				Priority Claimed			
	(number)	(country)	<del></del>	(day/month/year filed)	Yes	No		
	(number)	(country)	_	(day/month/year filed)	Yes	No		
	(application serial no.)	ween the filing date of such prior application a  (filing date)  (filing date)	nd the	aterial to patentability as defined in Title 37, Code of Federal d the national or PCT international filing date of this application:  (status - pending, patented or abandoned)  (status - pending, patented or abandoned)				
C	I hereby claim the benefit under Title 35, United States Code, § 119(e) of any United States provisional application(s) listed below:							
	(provisional application no.)	(filing date)						
•	I hereby appoint the following Registered Practitioners to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith:							
	David V. Trask, Reg. No. 22,012 Laurence B. Bond, Reg. No. 30,549 Allen C. Turner, Reg. No. 33,041 Stephen R. Christian, Reg. No. 32,687 Paul C. Oestreich, Reg. No. 44,983 Kenneth C. Booth, Reg. No. 42,342 Michael L. Lynch, Reg. No. 30,871	William S. Britt, Reg. No. 20,969 Joseph A. Walkowski, Reg. No. 28,765 Kent S. Burningham, Reg. No. 30,453 Brick G. Power, Reg. No. 38,581 Devin R. Jensen, Reg. No. 44,805 Samuel B. Webb, Reg. No. 44,394 Lia M. Pappas, Reg. No. 34,095		Thomas J. Rossa, Reg. No. 26,7 James R. Duzan, Reg. No. 28,3 Edgar R. Cataxinos, Reg. No. 3 Kenneth B. Ludwig, Reg. No. 4 Eleanor V. Goodall, Reg. No. 3 David L. Stott, Reg. No. 43,937	93 9,931 2,814 5,162			
	Address all correspondence to:  Brick G. Power, telephone no. (801) 532-1922.  TRASK, BRITT & ROSSA P.O. BOX 2550 Salt Lake City, Utah 84110							
	I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; nd further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or oth, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent statements.							
	Full name of first joint inventor: Vernon M. Inventor's signature  Residence: Meridian, Idaho	Villiams	Date	1/18/2000		<u> </u>		
	Residence: Meridian, Idaho Citizenship: U.S.A. Post Office Address: 4389 East Driftwood Dr							

## DECLARATION FOR PATENT APPLICATION

(continuation page)

Invention title: REINFORCED, SELF-ALIGNING CONDUCTIVE STRUCTURES FOR SEMICONDUCTOR DEVICE COMPONENTS AND METHODS FOR FABRICATING SAME

Inventor name(s) appearing on first declaration page: Vernon M. Williams

🛛 Additional original, first and joint inventor(s):

Full name of second joint inventor: Ford B. Grings Inventor's signature	Date	1/18/00
Residence: Meridian, Idaho		
Citizenship: U.S.A.		
Post Office Address: 1426 N. Tina Marie Avenue, Meridian, ID 83642		
Full name of third joint inventor: Bret K. Street Bolk Street	Date	1/19/200
Residence: Meridian, Idaho		

THE REAL PROPERTY AND THE PARTY OF THE PERSON OF THE PERSO

Citizenship: U.S.A. Post Office Address: 1366 N. Rutledge Avenue, Meridian, ID 83642